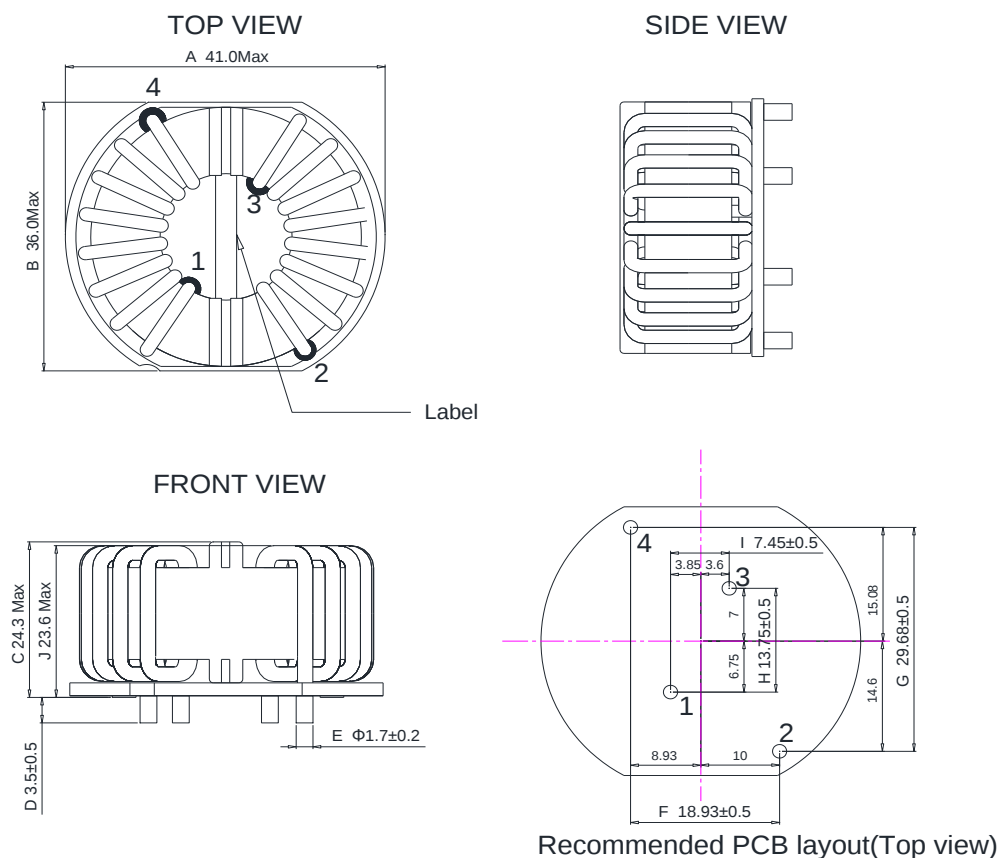


1. OUTLINE DRAWING(外形图): Unit(单位): mm (毫米)



NOTES(注):

- ◆PCB drill hole is recommended to be 2.3 mm (建议 PCB 板开孔孔径 2.3mm)
- ◆If PIN layout and footprint have slightly deviation, please refers to actual PCB assembly, the ones can be normally inserted into PCB is qualified (PIN 距、排距尺寸测量有偏差时, 以 PCB 下板实装确认, 可正常下板为合格)
- ◆The Pin length doesn't include the solder tip (PIN 脚长度不包括锡尖)
- ◆Pin length is measured from the highest point of the bottom surface(PIN 长从底板面的最高点起量).
- ◆Glue must not contact wire for solder. Pin must be free from glue, 引脚焊接部分不得有胶水。

2. MARKING (标记图):

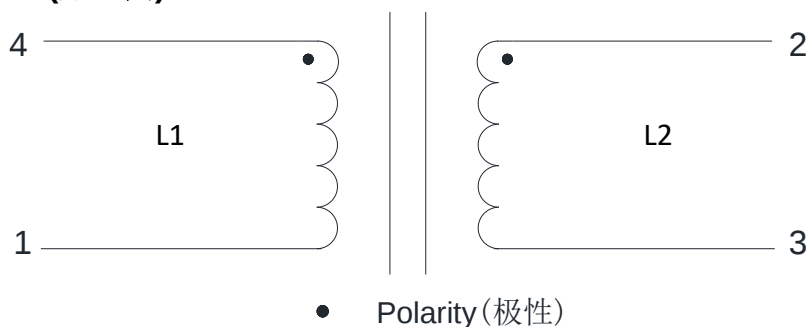
XZM-CM1020213
-Z00 YYWW D

YY:Year/年

WW:Week/周

D: 1-Monday , 2-Tuesday , 3- Wednesday , 4-Thursday , 5-Friday , 6-Saturday , 7-Sunday

3. SCHEMATIC(原理图):



4. ELECTRICAL SPECIFICATION(电气特性):

TEST TEMPERATURE(测试温度)@25±2℃:

Item 项目	Terminal 端子	Specification 规格值	Test condition 测试条件
Inductance 电感(uH)	4—1/ 2—3	900uH Min	100 kHz,0.3V,SER
Inductance 电感(uH)	4—1/ 2—3	900uH Min	250mA(DPG10)
Leakage inductance 漏感(uH)	4—1 (short 2,3)	4uH Min(4.5uHTyp).	100KHz,0.3V,SER
DCR 直流电阻(Ω)	4—1	4.5 mΩ Max	Ta=25℃
	2—3	4.5 mΩ Max	
Insulation Resistance 绝缘电阻	L1—L2	100 MΩ Min	1000VDC, 3sec
Dielectric strength 绝缘耐压	L1—L2	3410VDC 1min for qualification	3mA
		3410VDC 2sec in mass production	

5. PRECAUTIONS FOR USE (产品使用注意事项):

Ambient temperature range(环境温度范围): -40℃ to +80℃;

Operating Temperature range(工作温度范围): -40℃ to +150℃(Temperature rise included 包括温升);

Storage temperature range(保存温度范围): -40℃ to +125℃;

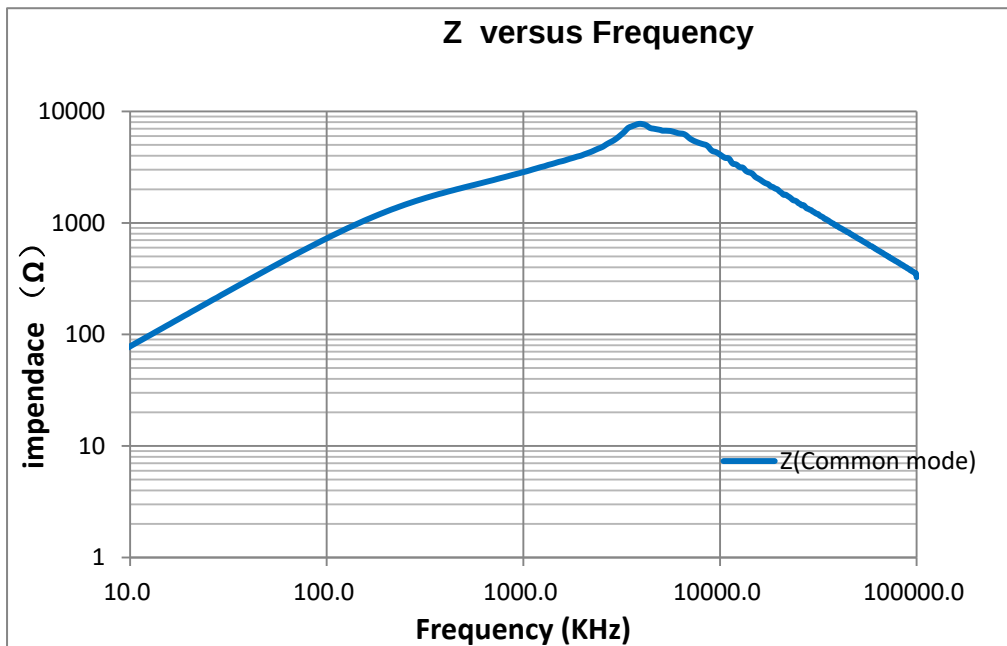
Resistance to solder heat(焊锡耐热性): +260±5℃, 5±1Sec;

Rohs compliant directive 2011/65/EU(产品符合欧盟 ROHS 2011/65/EU 指令);

Product complianct AEC-Q200, 产品符合 AEC-Q200 标准;

Rated Current(额定电流): 21ADC;

6.Electrical Parameters(Graph) 电气参数曲线图 Only for R&D reference 仅供研发参考



Pulse test :Inductance versus current

